

FIG.1

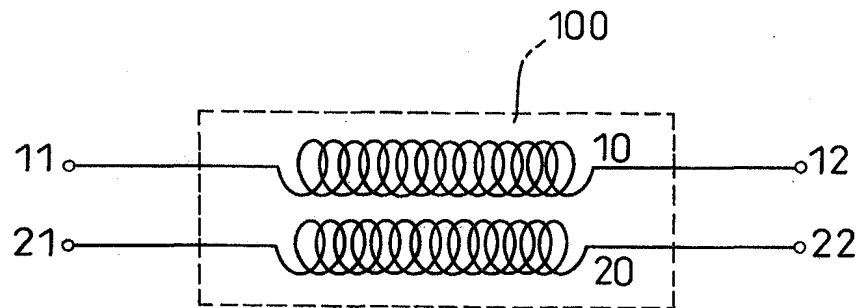


FIG. 2

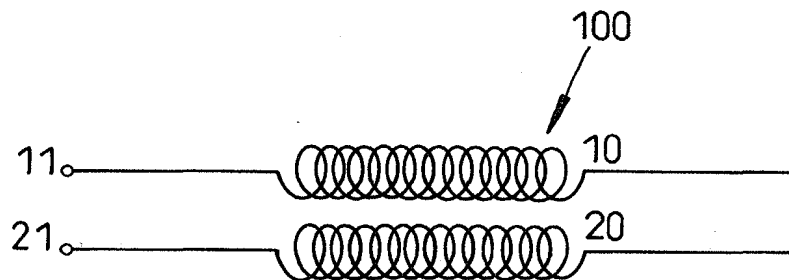


FIG. 3

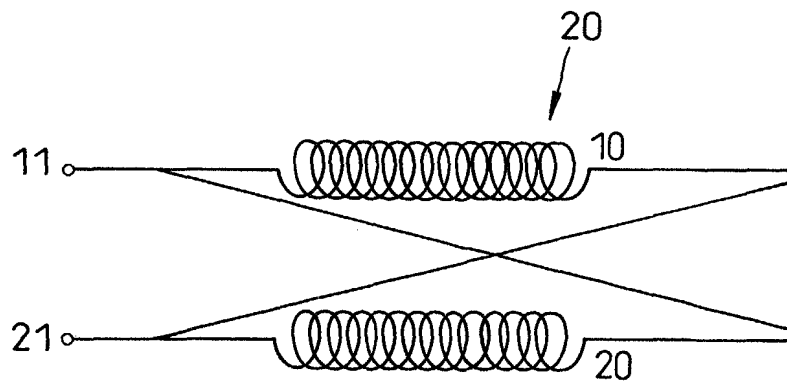


FIG. 4

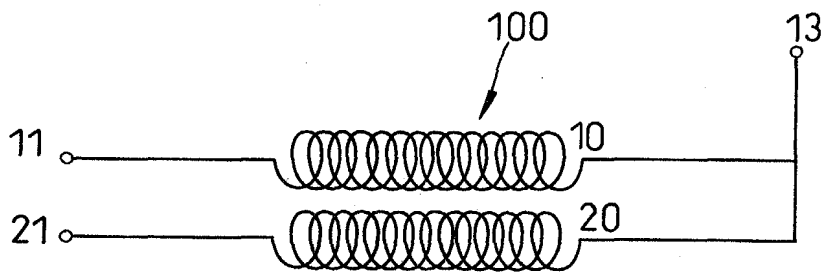


FIG. 5

FIG. 4

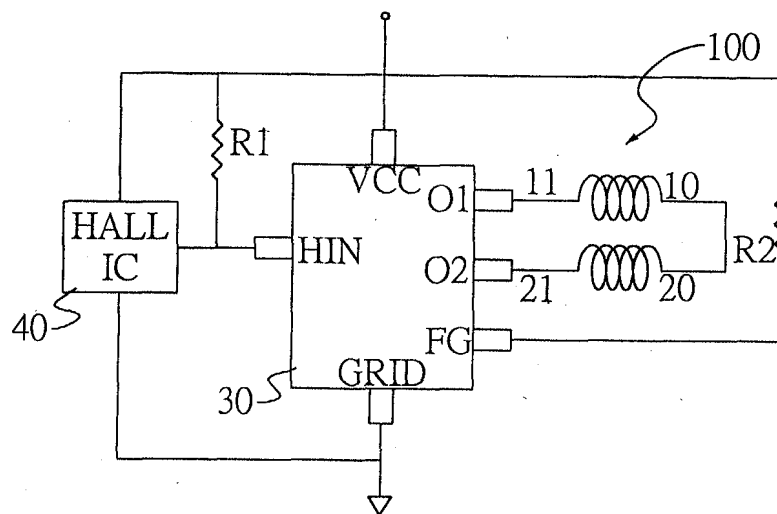
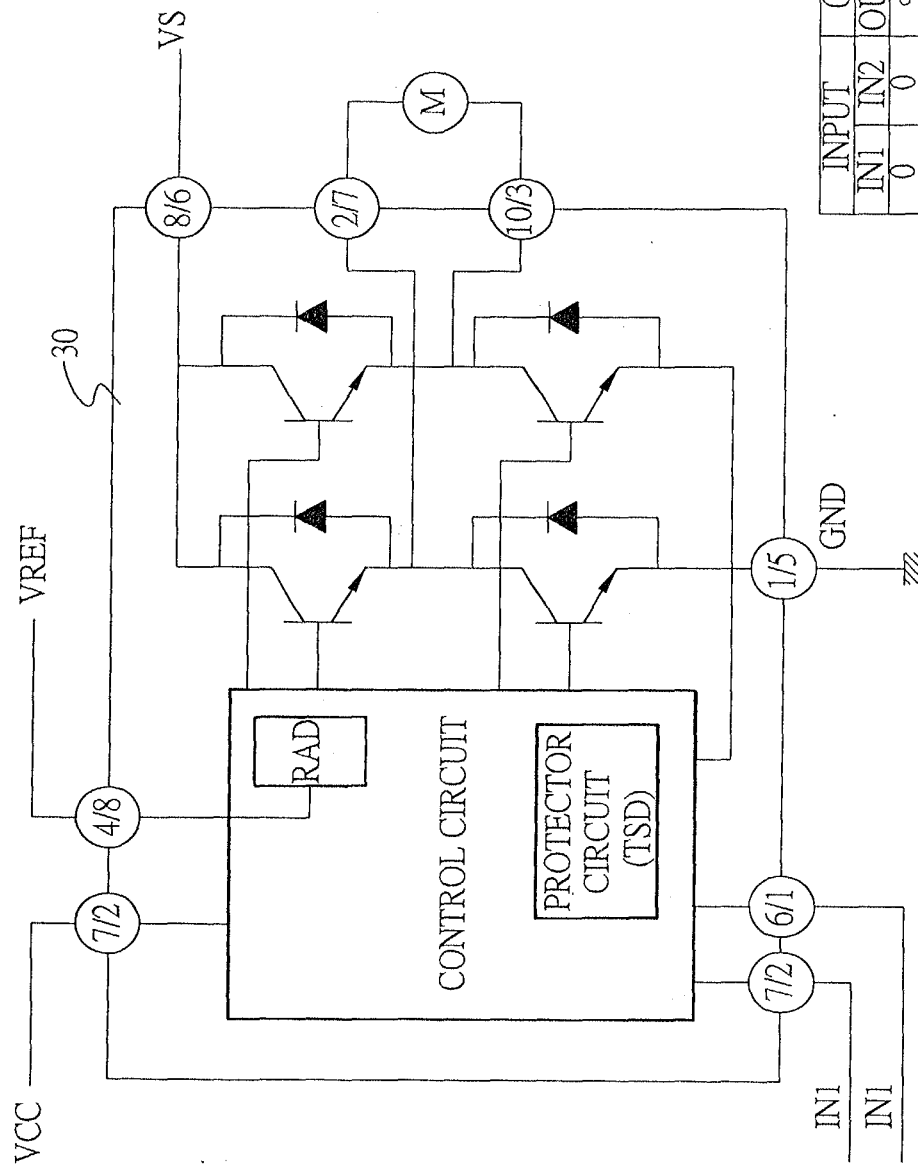


FIG. 6

[illegible]

MODE	INPUT		OUTPUT	
	IN1	IN2	OUT1	OUT2
STOP	0	0	∞	∞
CW CCW	1	0	H	L
CCW CW	0	1	L	H
BRAKE	1	1	L	L

FIG. 7